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|--------------------------------------|--|--|--|--|
| EFS ID: | 1573045 | | | |
| Application Number: | 10629302 | | | |
| International Application Number: | | | | |
| Confirmation Number: | 6438 | | | |
| Title of Invention: | Transceiver module and integrated circuit with dual eye openers and integrated loopback and bit error rate testing | | | |
| First Named Inventor/Applicant Name: | Lewis B. Aronson | | | |
| Customer Number: | 22913 | | | |
| Filer: | Peter Frederick Malen Jr./Gina Meredith | | | |
| Filer Authorized By: | Peter Frederick Malen Jr. | | | |
| Attorney Docket Number: | 15436.247.2.1.4 | | | |
| Receipt Date: | 07-MAR-2007 | | | |
| Filing Date: | 28-JUL-2003 | | | |
| Time Stamp: | 18:12:28 | | | |
| Application Type: | Utility | | | |

Payment information:

| Submitted with Payment | no | |
|------------------------|----|--|
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) | Multi Part /.zip | Pages (if appl.) |
|--------------------|--|---|------------------|---------------------|---------------------|
| 1 | Information Disclosure Statement (IDS) Filed | 15436_247_2_1_4_US_IDS _FormSB_08a.pdf | 673951 | no | 4 |
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| 3 | NPL Documents | New_Module_to_Slash_Cost .pdf | 151066 | no | 2 |
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| 4 | NPL Documents | Finisar_Press_Release.pdf | 134586 | no | 2 |
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| Information: | | | | | |
| 5 | NPL Documents | About_the_10Gigabit.pdf | 96872 | no | 2 |
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New International Application Filed with the USPTO as a Receiving Office

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